

Agenda: UK Design Forum 2003

Monday March 31st 2003		
19.00 – 20.30	Dinner at Chancellors	
20.30 – 21.00	After Dinner Speaker	
	John Vernon	From Valves to VLSI
21.00	Adjourn to Bar	
Tuesday April 1st 2003		
09.00 – 09.30	Invited Speaker	
	Bill Eccleston (25 mins)	Polymer Electronics: a rapidly growing market
09.30 – 11.15	The Grand Challenge	Session Chair: Andrew Brown
Speakers	Steve Furber (20 mins)	The House of Lords Report
	David Rees (20 mins)	The Grand Challenge
	Julie Stockwin (20 mins)	NMI role
	Susan Morrell (20 mins)	EPSRC Role
	DISCUSSION	
11.15 – 11.45	Poster and Coffee Break	
11.45 – 13.00	Issues for SMEs	Session Chair: Ted Pritchard
Speakers	Chris McArdle (20 mins)	The design needs of the UK SME community. Is China leading the way?
	David Topham (20 mins)	Analog Issues for SMEs
	Paul Wells (20 mins)	The case for outsourcing of silicon engineering
13.00 – 14.15	Lunch and Poster Session and further discussion	
14.15 – 15.30	Verification, Validation and Test	Session Chair: Hilton Kirk
Speakers	Kamal Hashmi (20 mins)	System Level Verification
	Mark Josephs (20 mins)	Verification of Asynchronous Circuits
	Bashir Al-Hashimi (20 mins)	Low-cost test for core-based system-on-a-chip
15.30 – 16.00	Tea and continuation of Poster Session	
16.00 – 16.40	Collaborations	Session Chair: Susan Morrell
	Tim Reynoldson (10 mins)	DTI Update
	Hilary Kahn (10 mins)	Electronic Design Research Web
	Jim Heaton (10 mins)	Electronic Design Programme
16.40	Wrap-up	